



九州工業大学

ディペンダブル集積システム研究センター (DISC)

## 特別講演のご案内

日時: 令和3年2月24日(水) 16:20~17:50

形式: Zoomを利用したオンライン講演

<https://kyutech-ac-jp.zoom.us/j/97349573023?pwd=MGpCOVBubjhMUHh6Q3pKLzcxZ2hjZz09>

Meeting ID: 973 4957 3023

Passcode: 571502

講師: Professor ZhiXiong DI (Southwest Jiaotong University, China)

題目: **Detailed Routing Congestion Prediction Technique**



In the modern physical implementation of VLSI circuits, routability has been a primary concern even more important than timing. As the manufacturing process continuously shrinks, how to accurately estimate routability at placement is becoming increasingly important. The early prediction in the physical design flow is critical to reduce design turnaround time and cost, and can avoid generating an unroutable design. Recently, machine learning techniques has been applied in this classic problem, and achieve exciting progress. This presentation focuses on the detailed routing congestion prediction and elimination technique, especially AI for short violation prediction.

Professor ZhiXiong DI is Associate Director of the Department of Electronic Engineering in the school of School of Information Science and Technology Southwest Jiaotong University. He received the Ph.D degree in microelectronic and solid electronics from Xidian University in 2014. His current research interests include High-performance VLSI/SoC design methodology, CAD algorithm of digital IC physical implementation.

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