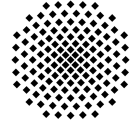




## 2016 Joint Workshop on Dependable Integrated Systems



September 9, 2016

Conference Room E, Denki Building, 2-1-82, Watanabe Ave., Chuo-Ku, Fukuoka, Japan

■ 13:25~13:30 Opening

*S. Kajihara, Kyushu Institute of Technology, Japan*

■ 13:30~14:00 Presentation 1

- Secure and Efficient Hardware Infrastructure Access  
*M. A. Kochte, University of Stuttgart, Germany*

■ 14:00~14:30 Presentation 2

- Mapping Delay Value Increased by IR-Drop to Layout Information  
*K. Miyase, Kyushu Institute of Technology, Japan*

■ 14:30~15:00 Break

■ 15:00~15:30 Presentation 3

- Diagnosis of Small Delay Faults  
*E. Schneider, University of Stuttgart, Germany*

■ 15:30~16:00 Presentation 4

- Formal Test Point Insertion for Region-based Low-Capture-Power Compact At-Speed Scan Test  
*S. Holst, Kyushu Institute of Technology, Japan*

■ 16:00~17:00 Project Review and Planning

*Kyushu Institute of Technology, Japan  
University of Stuttgart, Germany*

■ 17:00~17:05 Closing

*X. Wen, Kyushu Institute of Technology, Japan*